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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Mark Pavier

Date: November 19, 2004

Serial No.: 10/050,002

Group Art Unit: 2815

Filed: January 15, 2002

Examiner: Chris C. Chu

Allowed: September 14, 2004

Confirmation No. 5495

For: BACK-TO-BACK CONNECTED POWER SEMICONDUCTOR DEVICE PACKAGE

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

FORMAL DRAWINGS

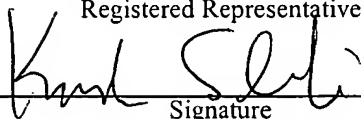
Sir:

In response to the Notice of Allowability dated September 14, 2004, enclosed herewith please find two (2) sheets of formal drawings containing Figures 1 through 7 for the above-identified application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Issue Fee, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 19, 2004:

Kourosh Salehi

Name of applicant, assignee or
Registered Representative

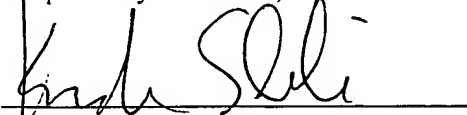


Signature

November 19, 2004

Date of Signature

Respectfully submitted,



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Enclosure

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